

# C Type XO

ROHS Compliant Standard



## FEATURE

- 1、7.5×5.0×1.2mm SMD package.
- 2、Tight symmetry(45 to 55%)available.



## Numbering system



Supply Voltage
C: 5V
E: 3.3V

Output Logic and symmetry				
	TTL	TTL 50pF	CMOS 15pF	CMOS 50pF
50±5%	A	E	J	F
50±10%	B	R	K	G

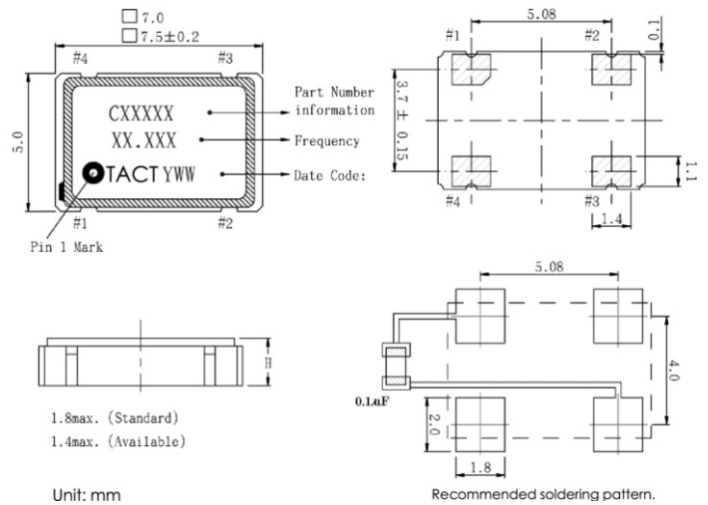
Tri-State Function
F: Fixed-Frequency Without Tri-State
T: Fixed-Frequency With Tri-State

Temperature Range
B: 0°C to +55°C
I: -10°C to +60°C
C: -20°C to +70°C
L: -40°C to +85°C

Frequency Stability
D: ±25ppm
G: ±50ppm
H: ±100ppm

## SPECIFICATION

Parameter	5v±10%	3.3±10%
Frequency range (MHz)	1.5~125	
Operating Temp. range(°C)	Refer to Numbering System	
Frequency Stability *	Refer to Numbering System	
Supply Current (mA) max.		
1.5MHz ≤ F <sub>0</sub> < 20MHz	10	7
20MHz ≤ F <sub>0</sub> < 50MHz	30	20
50MHz ≤ F <sub>0</sub> < 125MHz	40	30
Transition Time+: Rise/Fall (ns) max.		
1.5MHz ≤ F <sub>0</sub> < 20MHz	8	10
20MHz ≤ F <sub>0</sub> < 125MHz	5	6
Storage Temp. Range (°C)	-40~+125	



\*Inclusive of calibration at 25°C, operating temperature range, input voltage variation, load variation, aging, shock, and vibration  
 +Transition times are measured between 10% and 90% of V<sub>DD</sub>' with an output load of 15pF.

## FBEQ. STABILITY VS. TEMP. PANGE

Temp.(.c)	PPM	D: ±25	G: ±50	H: ±100
B	0~+55	○	○	○
I	-10~+60	○	○	○
C	-20~+70	○	○	○
L	-40~+85	×	○	○

○:Standard △:Available(case by case) ×:Not available

PIN	FUNCTION
#1	N.C./3-STATE
#2	GND
#3	OUTPUT
#4	VDD